

INFORMATION DISCLOSURE STATEMENT PTO-1449 (PAGE 1 OF 1)		SERIAL NUMBER 10/629,793	DOCKET NO. P56905
		APPLICANT YOSHITAKA TERAO et al.	
		FILING DATE 30 July 2003	GROUP 2879

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100-1449

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U.S. PATENT DOCUMENTS

EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
AW	RE37,444	11/01	Kanazawa			
AW	5,541,618	07/96	Shinoda			
AW	5,661,500	08/97	Shinoda et al.			
AW	5,663,741	09/97	Kanazawa			
AW	5,674,553	10/97	Sinoda et al.			
AW	5,724,054	03/98	Shinoda			
AW	5,786,794	07/98	Kishi et al.			
AW	5,952,782	09/99	Nanto			
AW	6,630,916	10/03	Shinoda			
AW	6,707,436	03/04	Setoguchi et al.			

FOREIGN PATENT DOCUMENTS

TRANSLATION

	DOCUMENT NUMBER	DATE	COUNTRY	CLAS	SUBCLASS	YES	NO
	JP 2001-325888	11/01	JAPAN			Abstract	
	JP 2001-043804	02/01	JAPAN			Abstract	
	JP 2917279	04/99					
	JP 02-148645	06/90	JAPAN			Abstract	
	JP 2845183	10/98					

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	<p>"Final Draft International Standard", Project No. 47C/61988-1/Ed.1; Plasma Display Panels - Part 1: Terminology and letter symbols, published by International Electrotechnical Commission, IEC. in 2003, and Appendix A - Description of Technology, Annex B - Relationship Between Voltage Terms And Discharge Characteristics; Annex C - Gaps and Annex D - Manufacturing no month</p>	

EXAMINER: Joseph Williams

DATE CONSIDERED: 4/4/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.